

Abstract

A packaging container for electronic components, particularly integrated circuits, which includes a tray into which the integrated circuits can be secured, a tray cover which is secured onto the tray, wherein the tray cover is composed of a plastic material, a desiccating material and an electrostatic dissipating product. The tray cover may further include a humidity indicating system incorporated into the tray cover. The packaging container may also be placed within a water and moisture-proof barrier bag for shipment purposes.

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